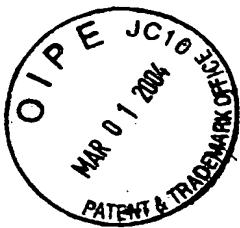


PATENT
Docket No. 20059/PIA31194



IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE

Applicant: Cheolsoo Park) I hereby certify that this paper is
Serial No.: 10/749,485) being deposited with the United
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Semiconductor Device") mail in an envelope addressed to:
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) 1450 on this date:
February 27, 2004
Mark G. Hanley
Mark G. Hanley
Registration No. 44,736
Attorney for Applicant(s)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

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Respectfully submitted,

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February 27, 2004

Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office		Atty. Docket No. 20059/PIA31194	Serial No. 10/749,485
INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>		Applicant Cheolsoo Park		
		Filing Date December 30, 2003	Group Art Unit Unknown	

U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01	6,130,102	10/10/2000	White, Jr. et al.			
	A02	6,448,649	9/10/2002	Lee et al.			
	A03						
	A04						
	A05						
	A06						
	A07						
	A08						
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	A12						
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	A15						
	A16						
	A17						
	A18						
	A19						
	A20						
	A21						
	A22						

EXAMINER	DATE CONSIDERED
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to applicant.</p>	